



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gerd Frankowsky et al.  
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Title : SEMICONDUCTOR CIRCUIT MODULE AND METHOD FOR FABRICATING  
SEMICONDUCTOR CIRCUIT MODULES

## MAIL STOP ISSUE FEE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

COMMENTS ON EXAMINER'S REASONS FOR ALLOWANCE

The applicant notes that there may be other good reasons for the patentability of the allowed claims, and other claims.

Respectfully submitted,

Date: 1/12/06

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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